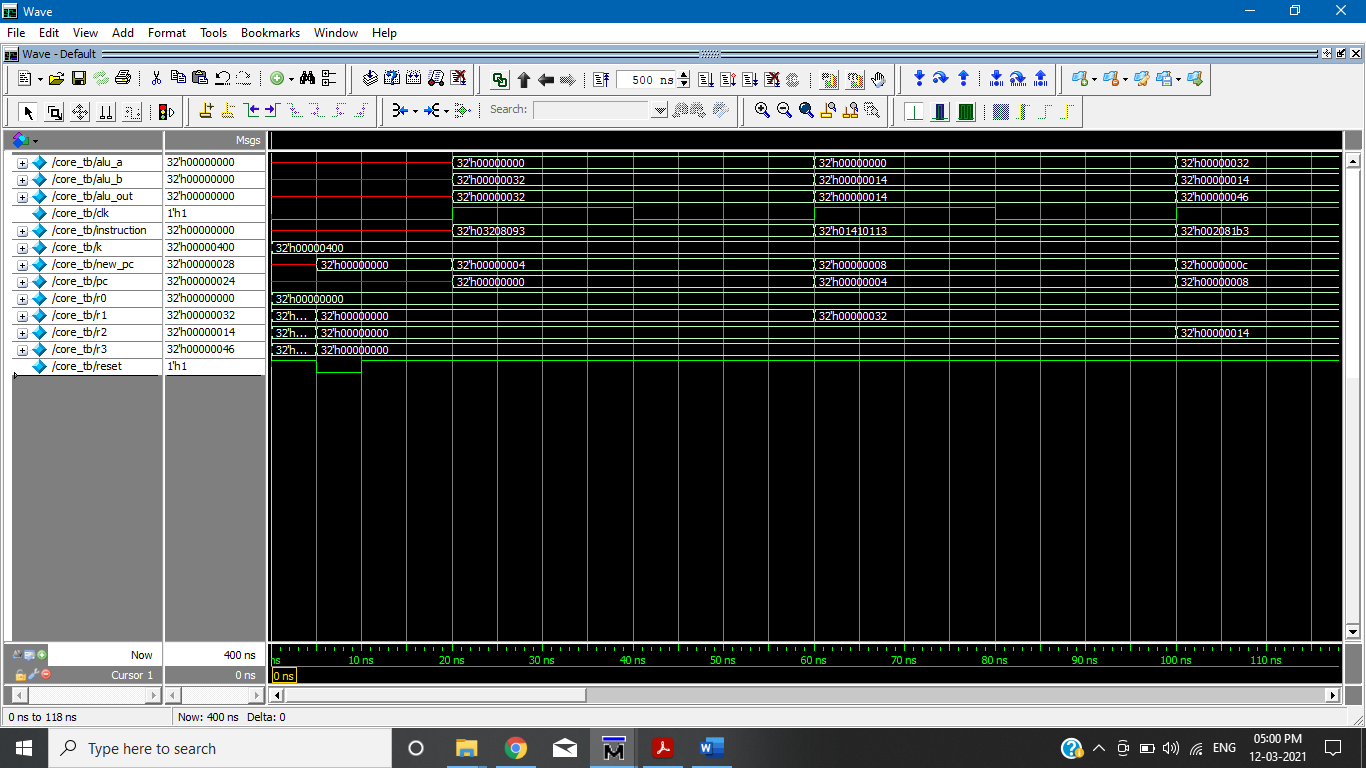
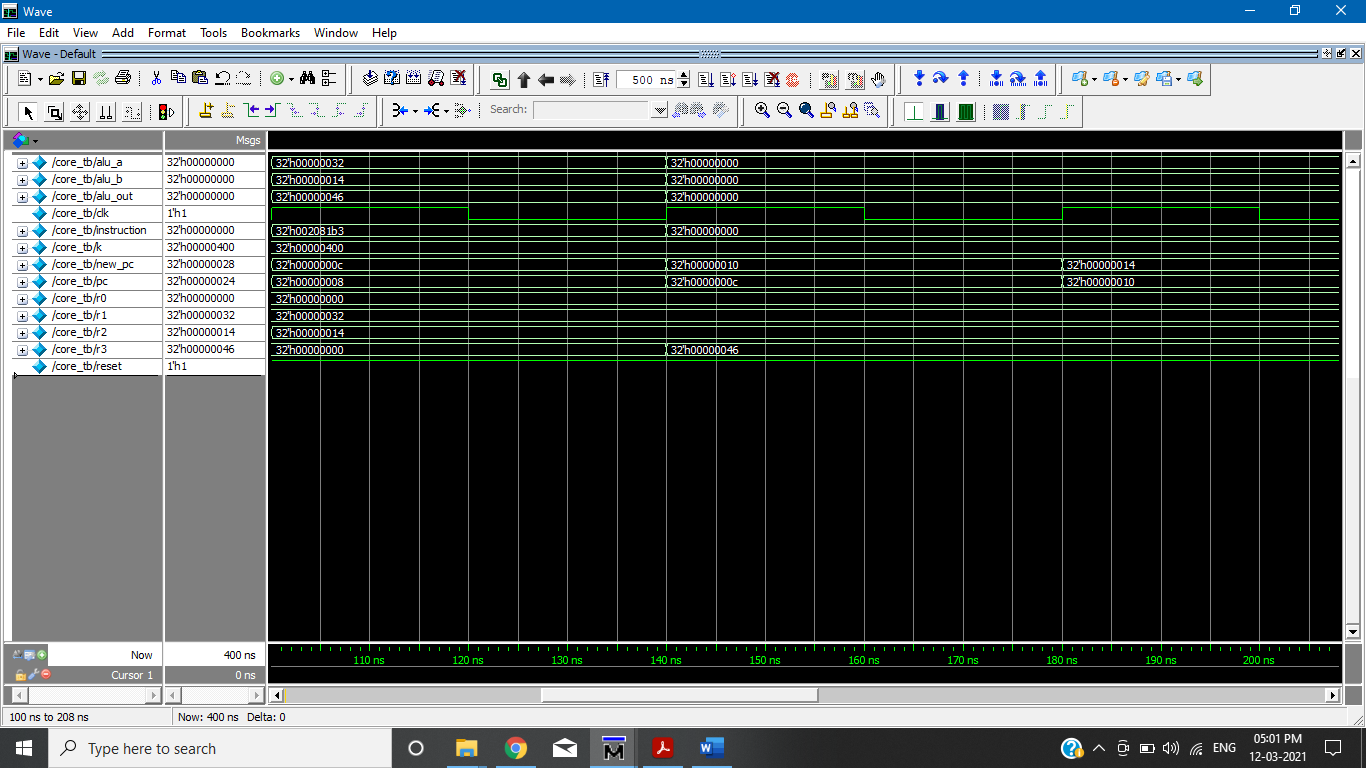
**Instruction Testing for R-Type and I-Type Instruction**

|  |  |  |  |
| --- | --- | --- | --- |
| Serial Number | Testbench Name | Problem Name | Instructions |
| 1 | core\_tb\_1 | ADD | addi, add |
| 2 | core\_tb\_2 | SUB | addi, sub |
| 3 | core\_tb\_3 | SLL | addi, sll |
| 4 | core\_tb\_4 | SRL | addi,srl |
| 5 | core\_tb\_5 | SRA | addi,sra |
| 6 | core\_tb\_6 | SLT | addi,slt |
| 7 | core\_tb\_7 | SLTU | addi,sltu |
| 8 | core\_tb\_8 | OR | addi,or |
| 9 | core\_tb\_9 | AND | addi,and |
| 10 | core\_tb\_10 | XOR | addi,xor |
| 11 | core\_tb\_11 | SLTI | addi,slti |
| 12 | core\_tb\_12 | SLTUI | addi,sltui |
| 13 | core\_tb\_13 | XORI | addi,xori |
| 14 | core\_tb\_14 | ORI | addi,ori |
| 15 | core\_tb\_15 | ANDI | addi,andi |
| 16 | core\_tb\_16 | SLLI | addi,slli |
| 17 | core\_tb\_17 | SRLI | addi,srli |
| 18 | core\_tb\_18 | SRAI | addi,srai |

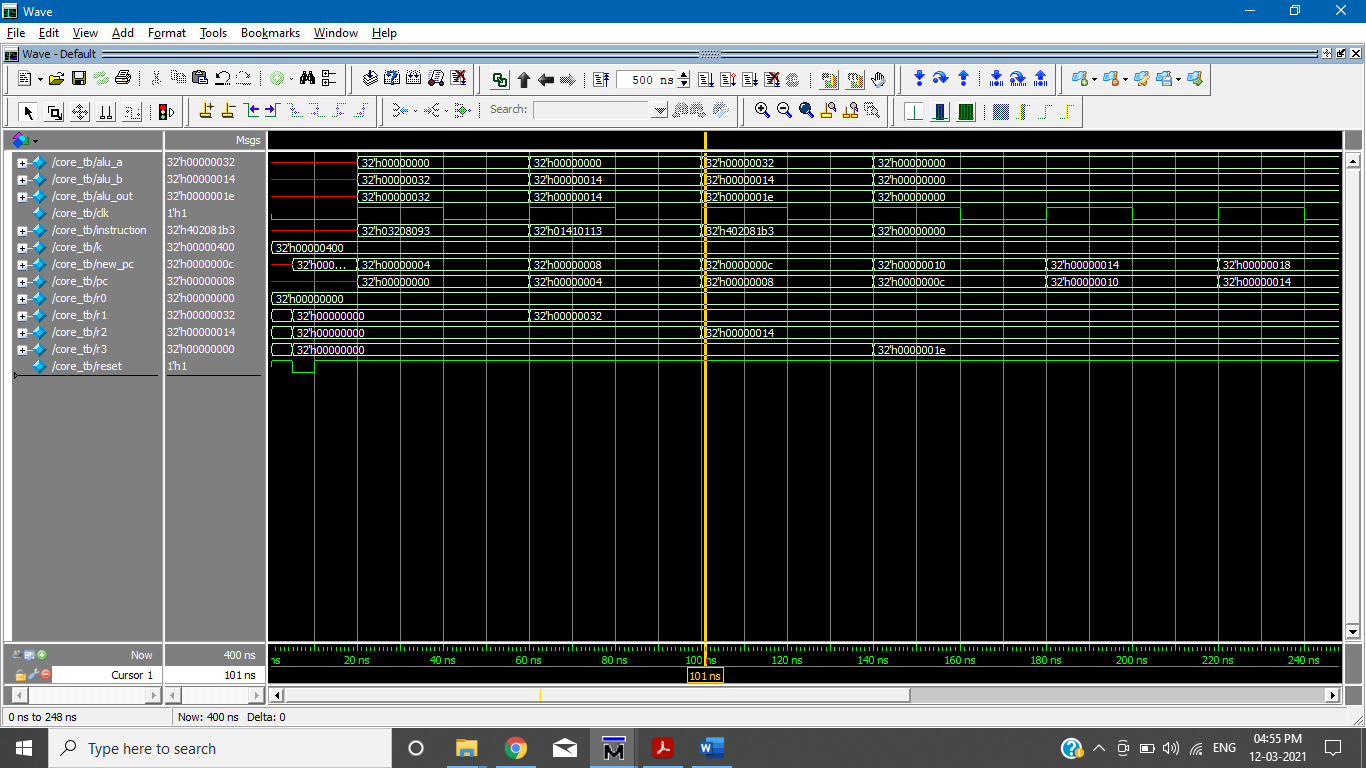
**RESULTS**

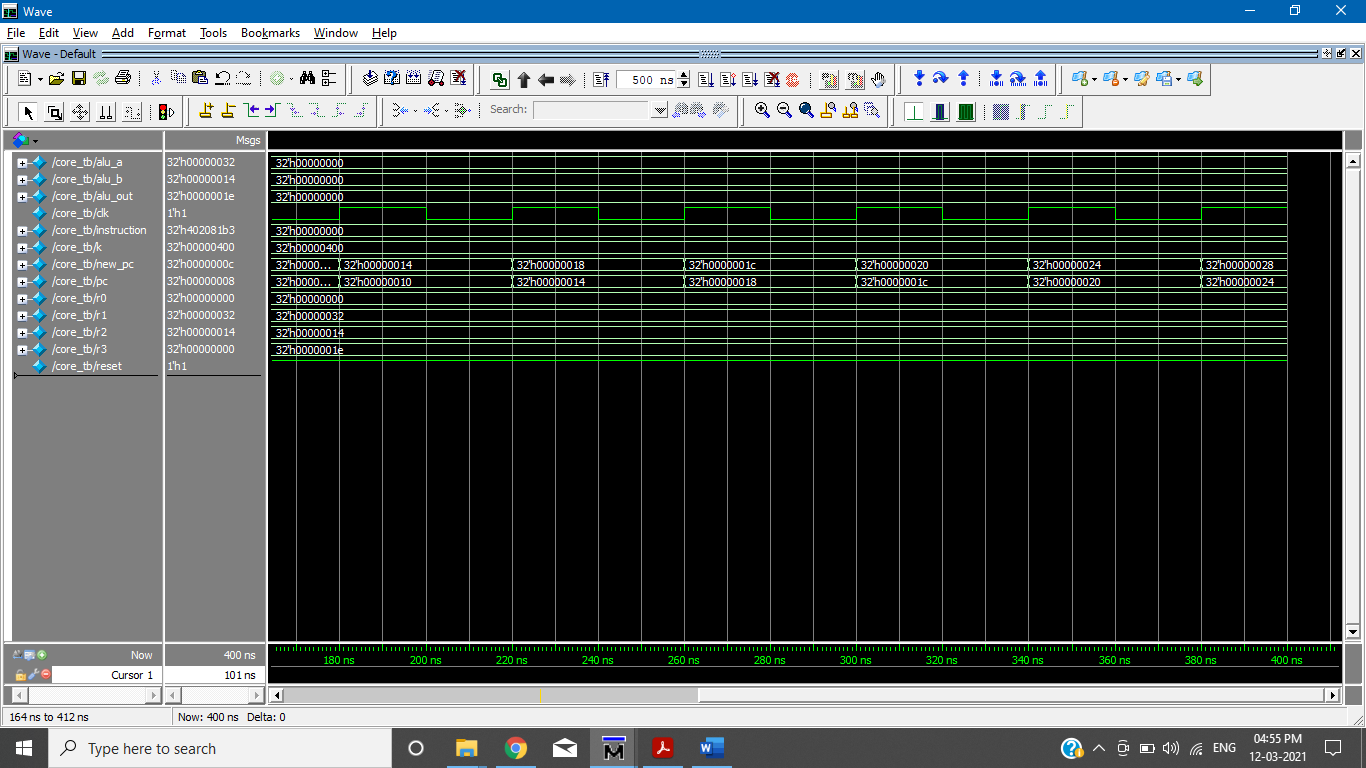
* **ADD**



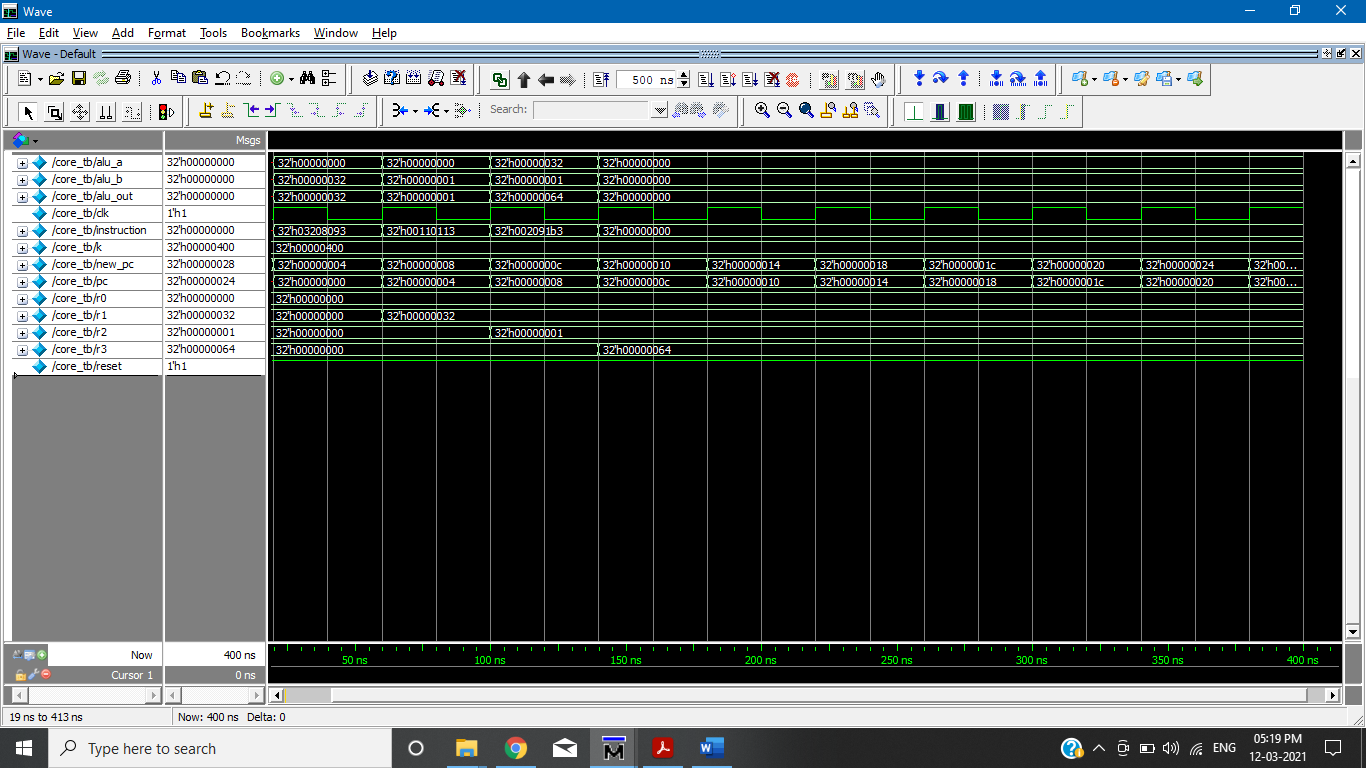


* **SUB**

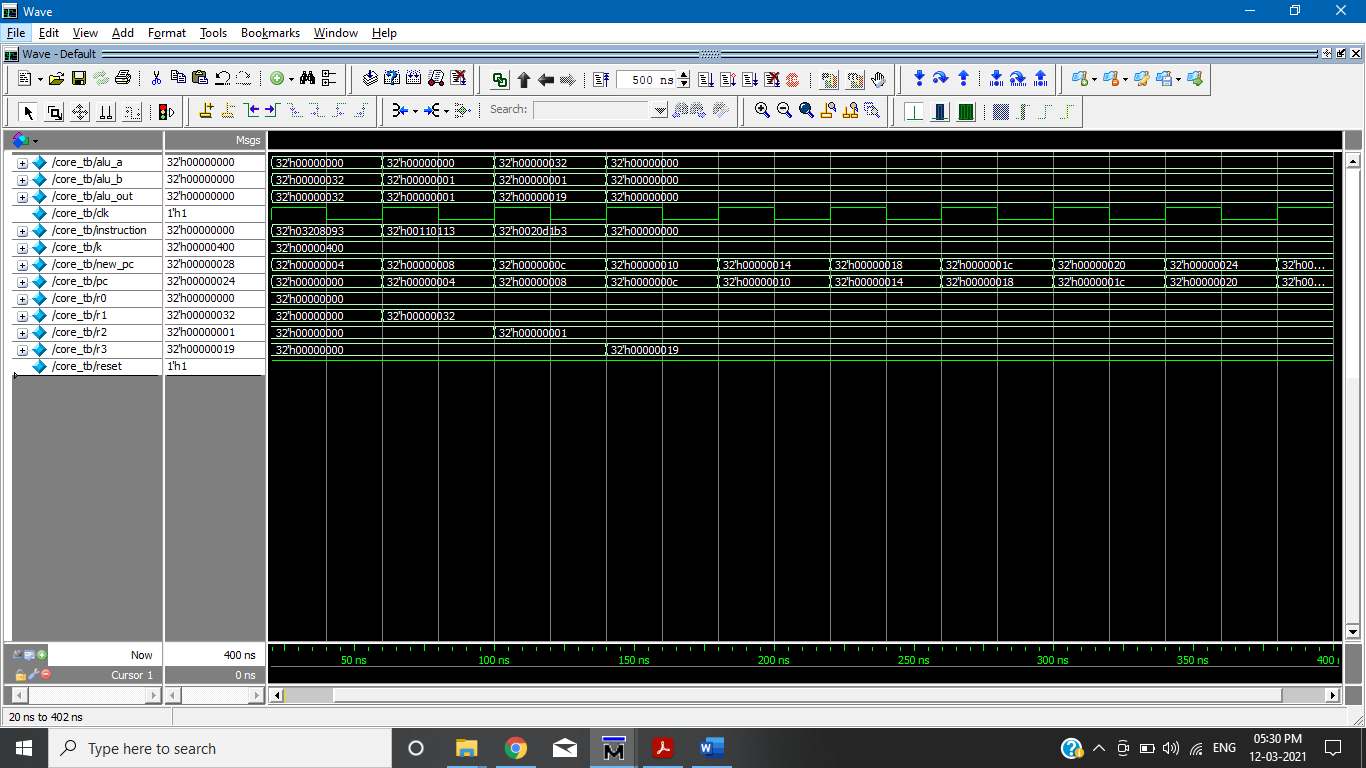




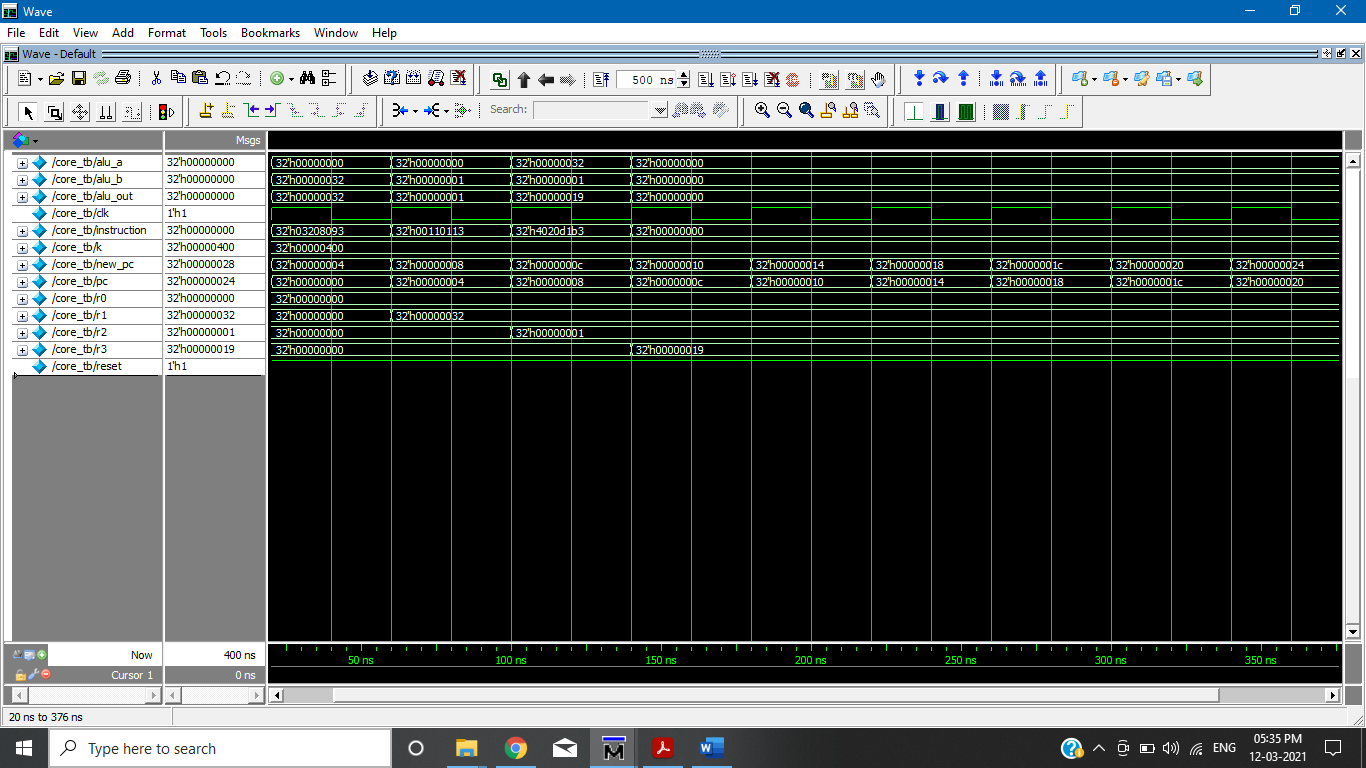
* **SLL**



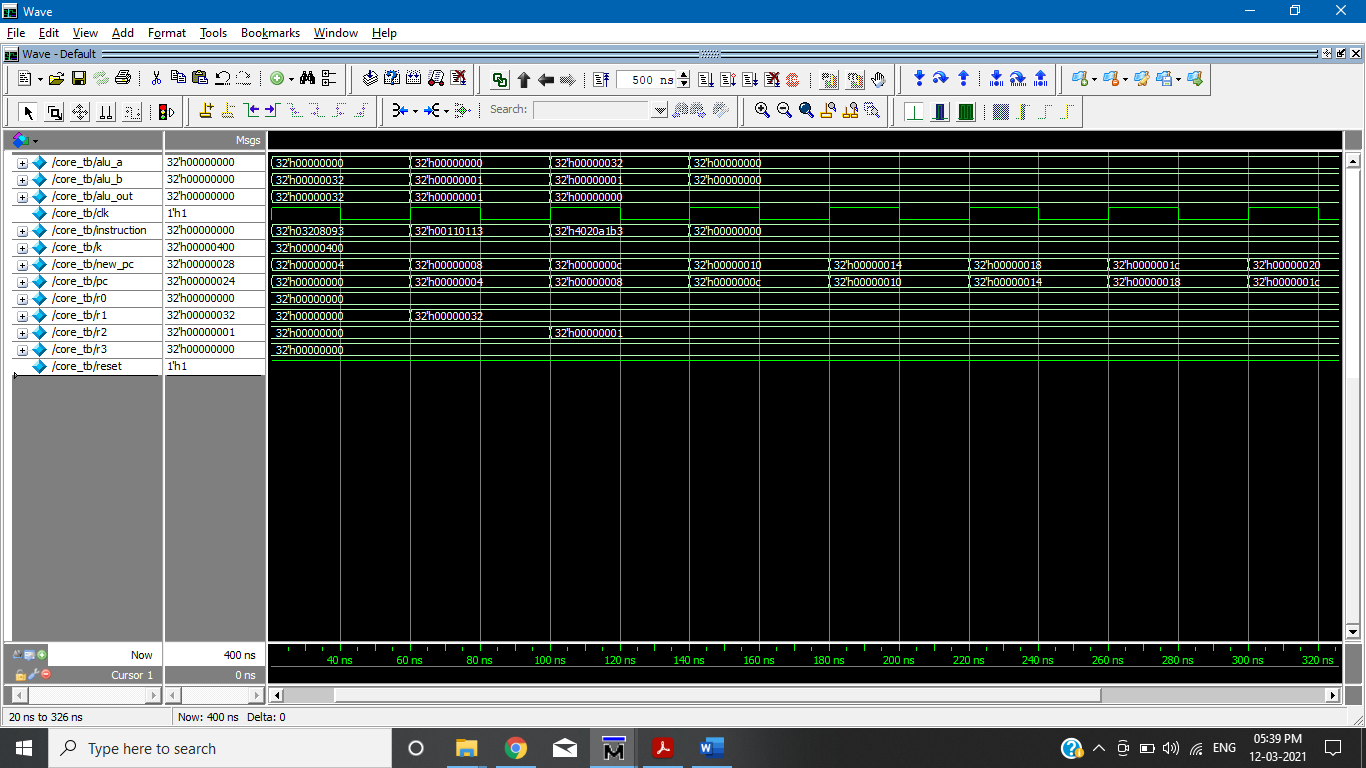
* **SRL**



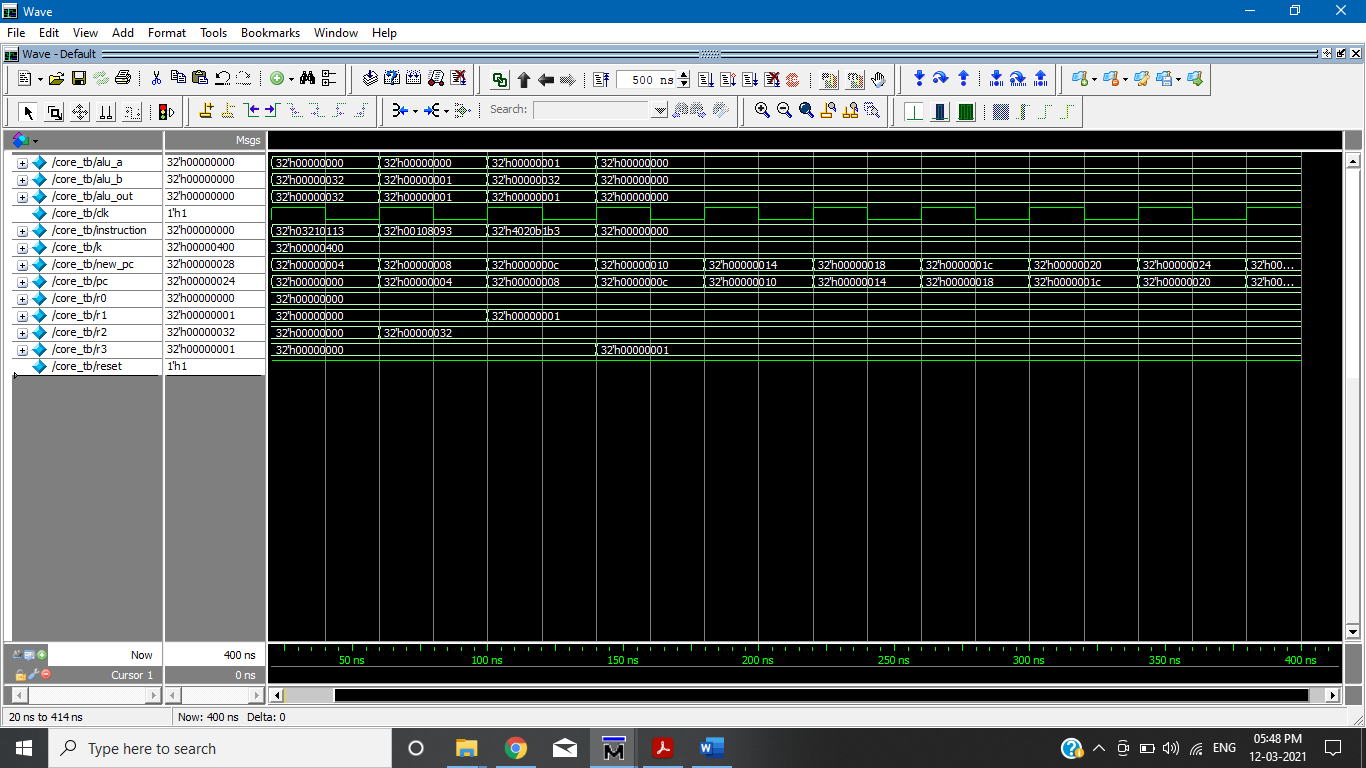
* **SRA**



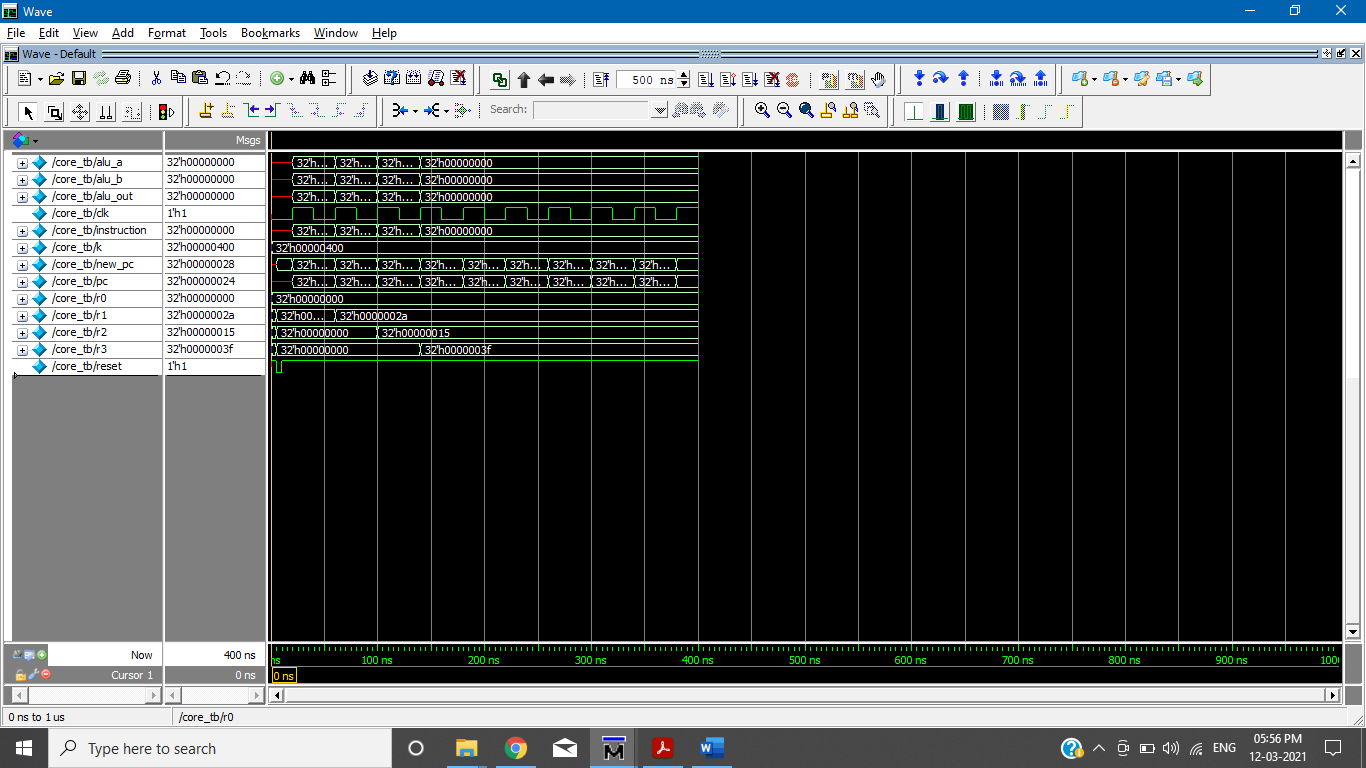
* **SLT**



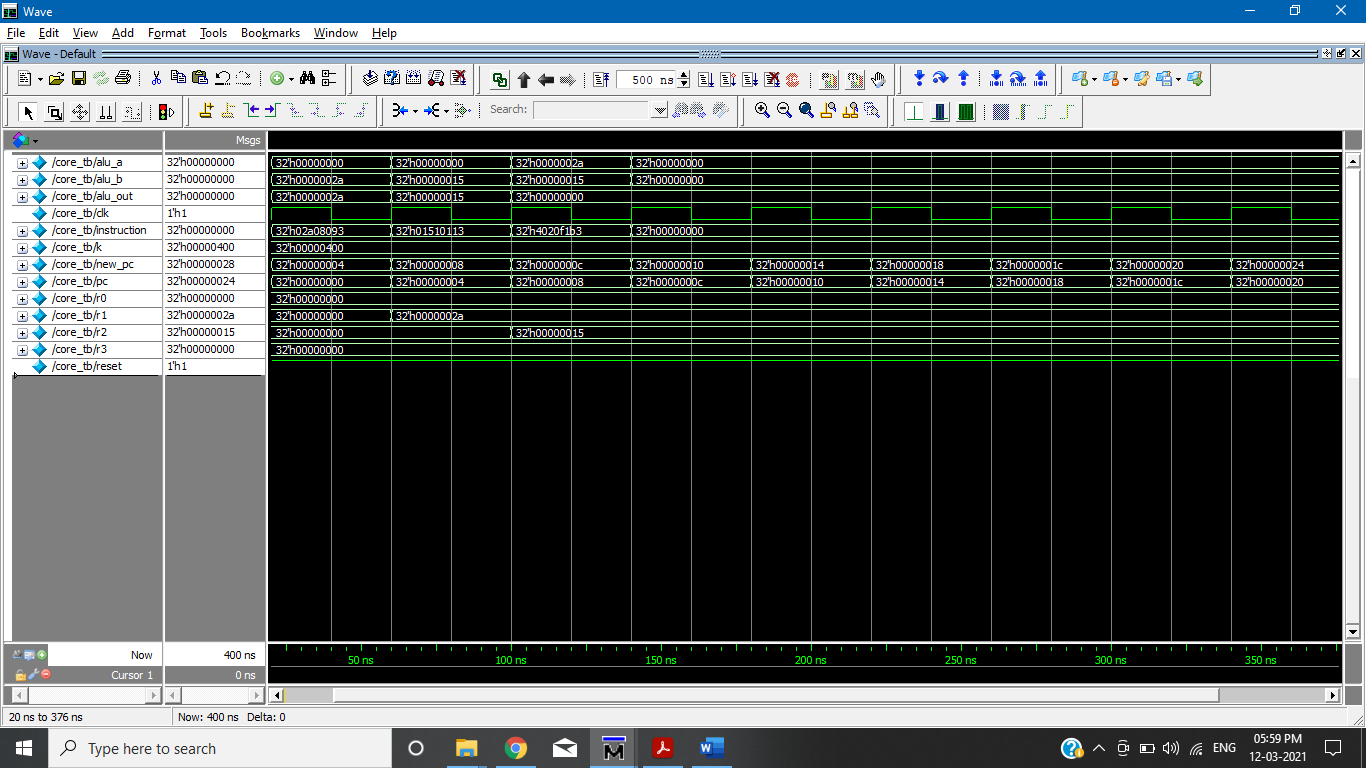
* **SLTU**



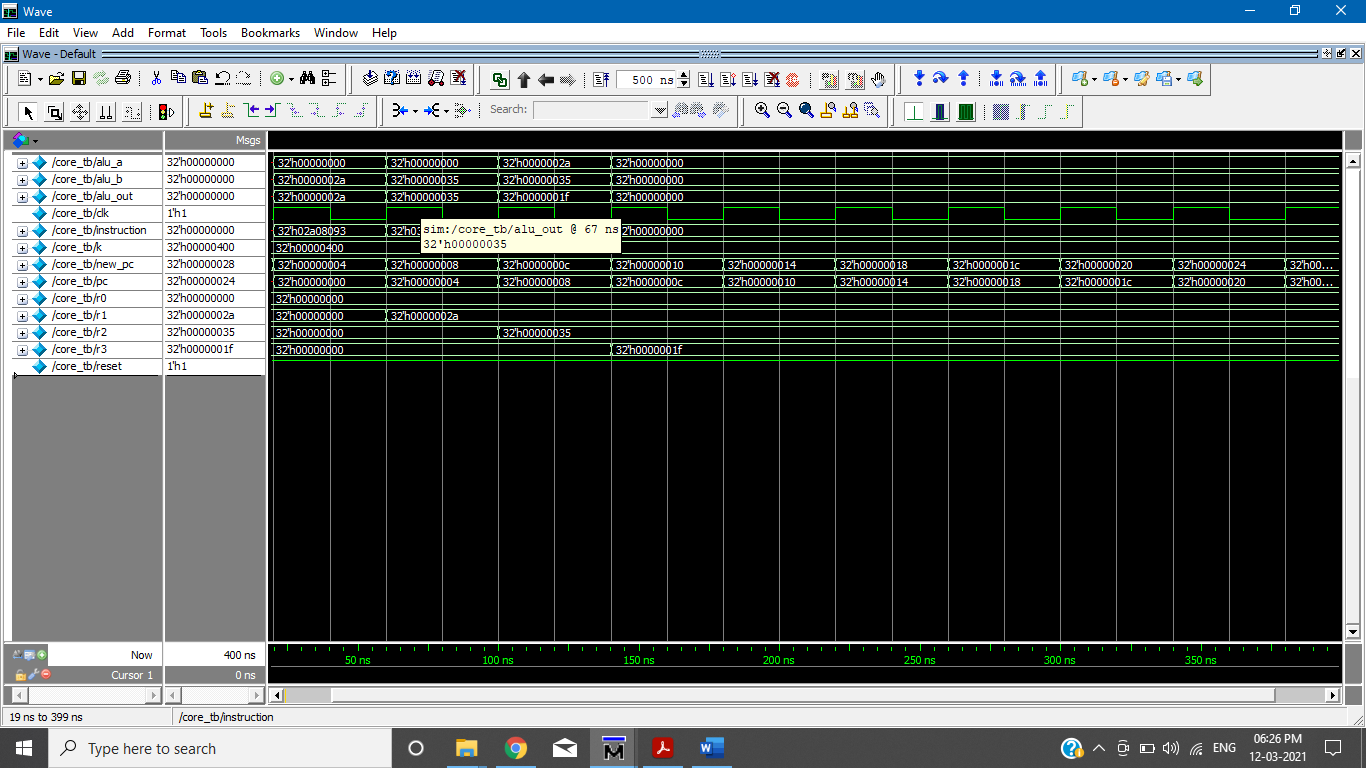
* **OR**



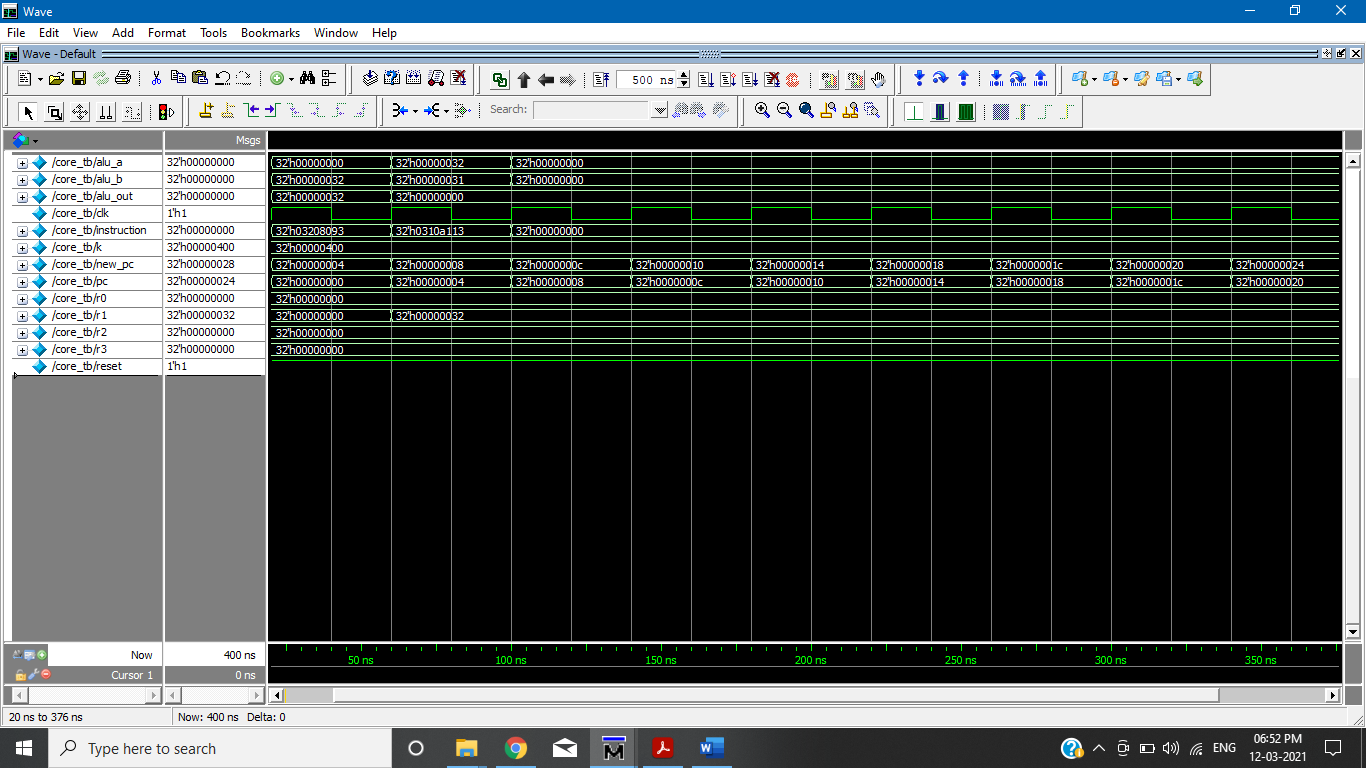
* **AND**



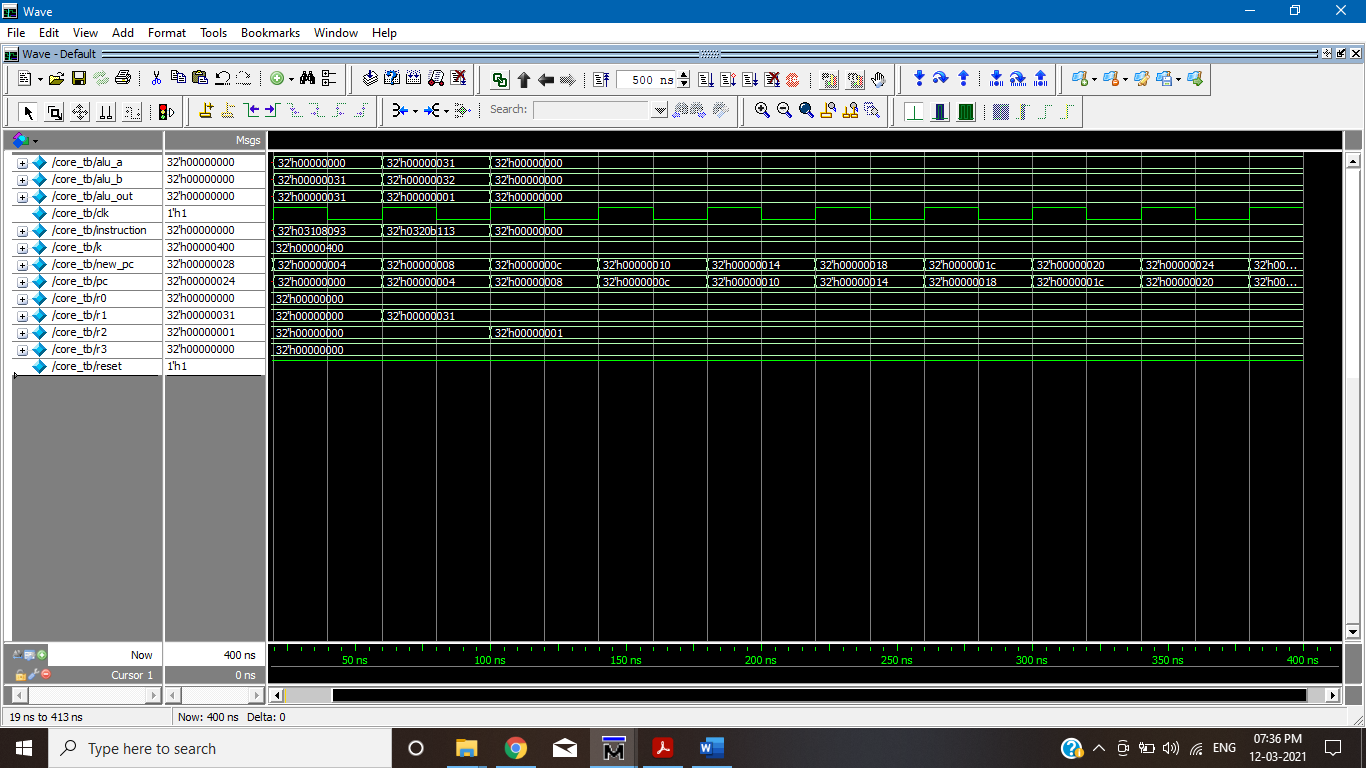
* **XOR**



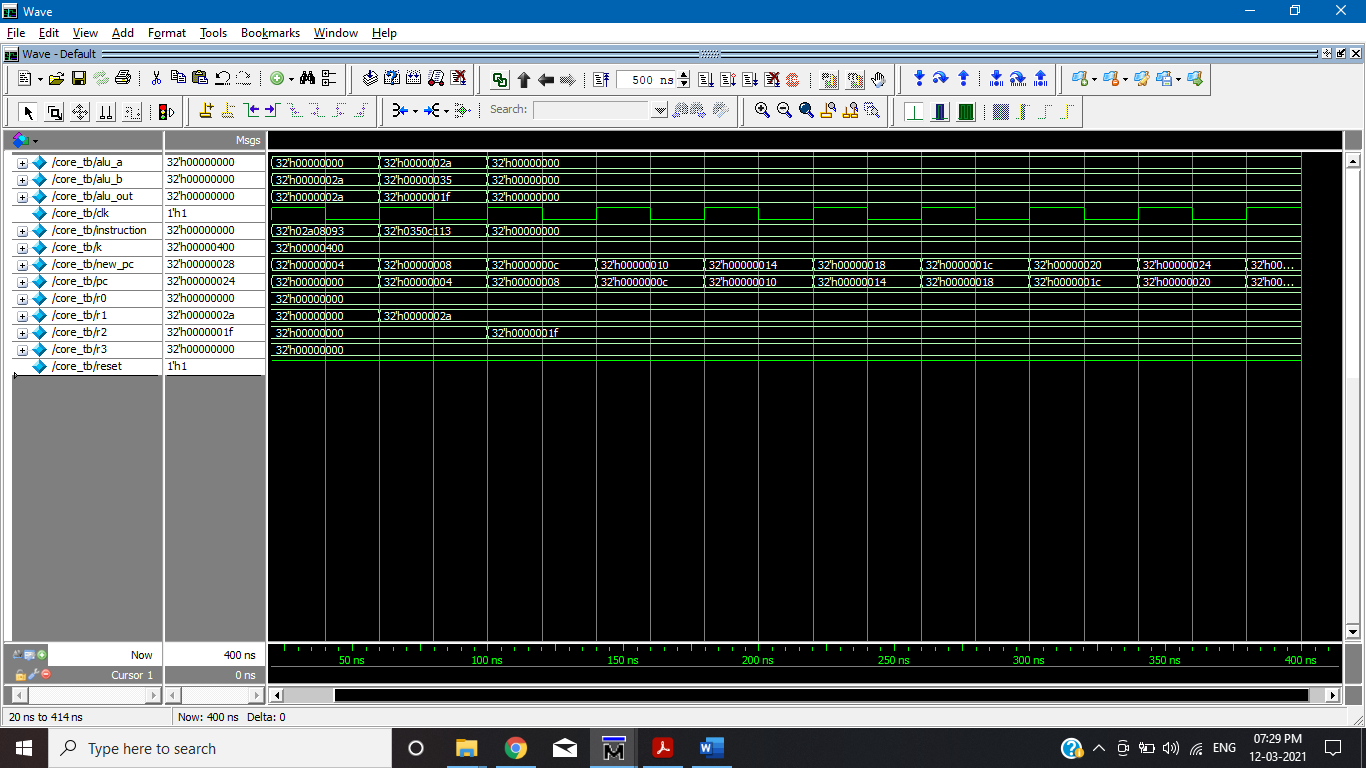
* **SLTI**



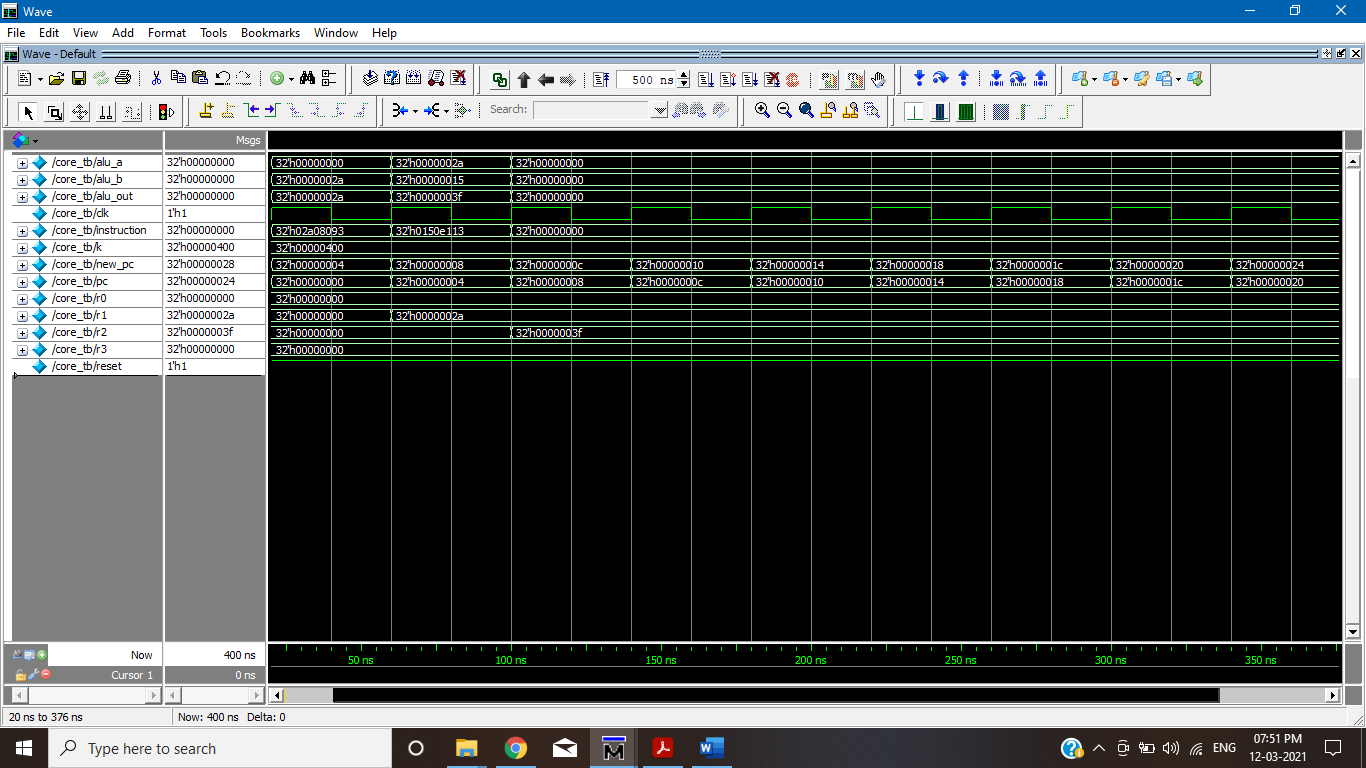
* **SLTUI**



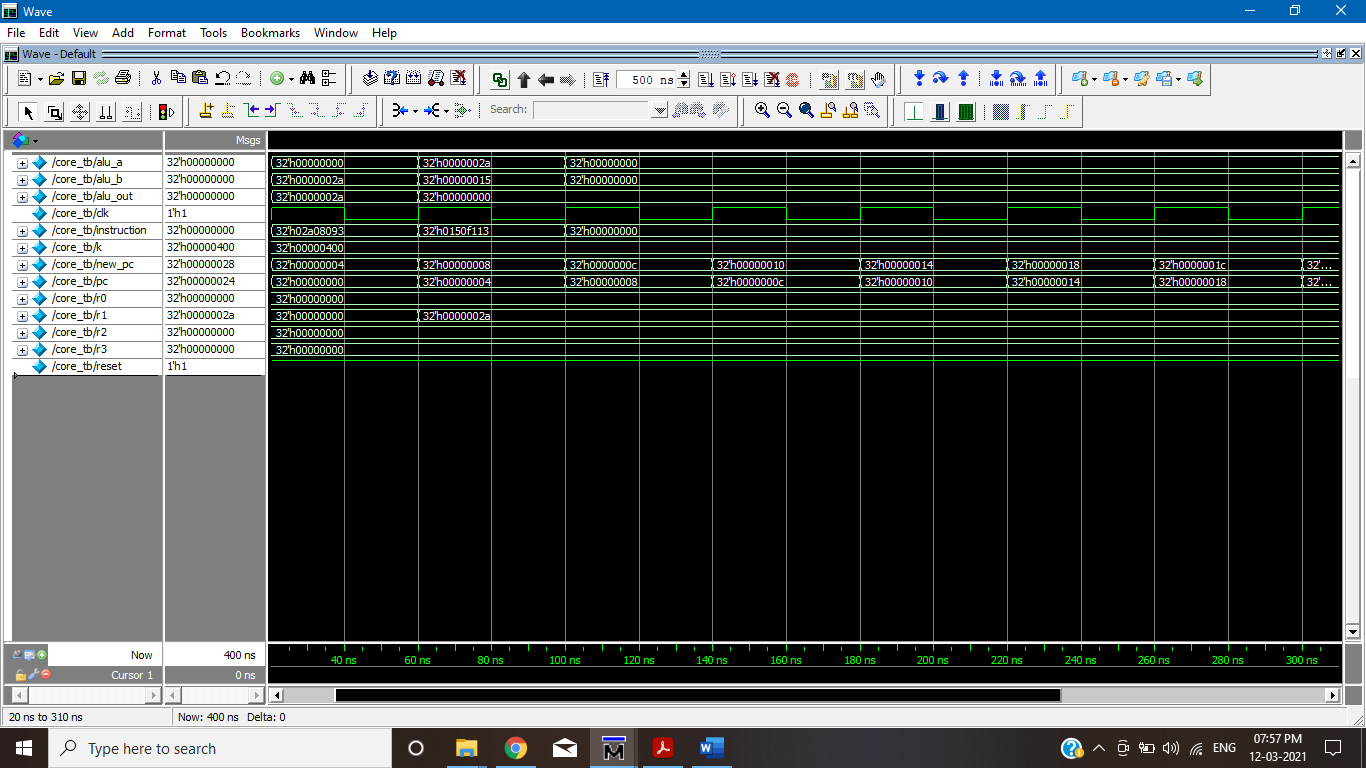
* **XORI**



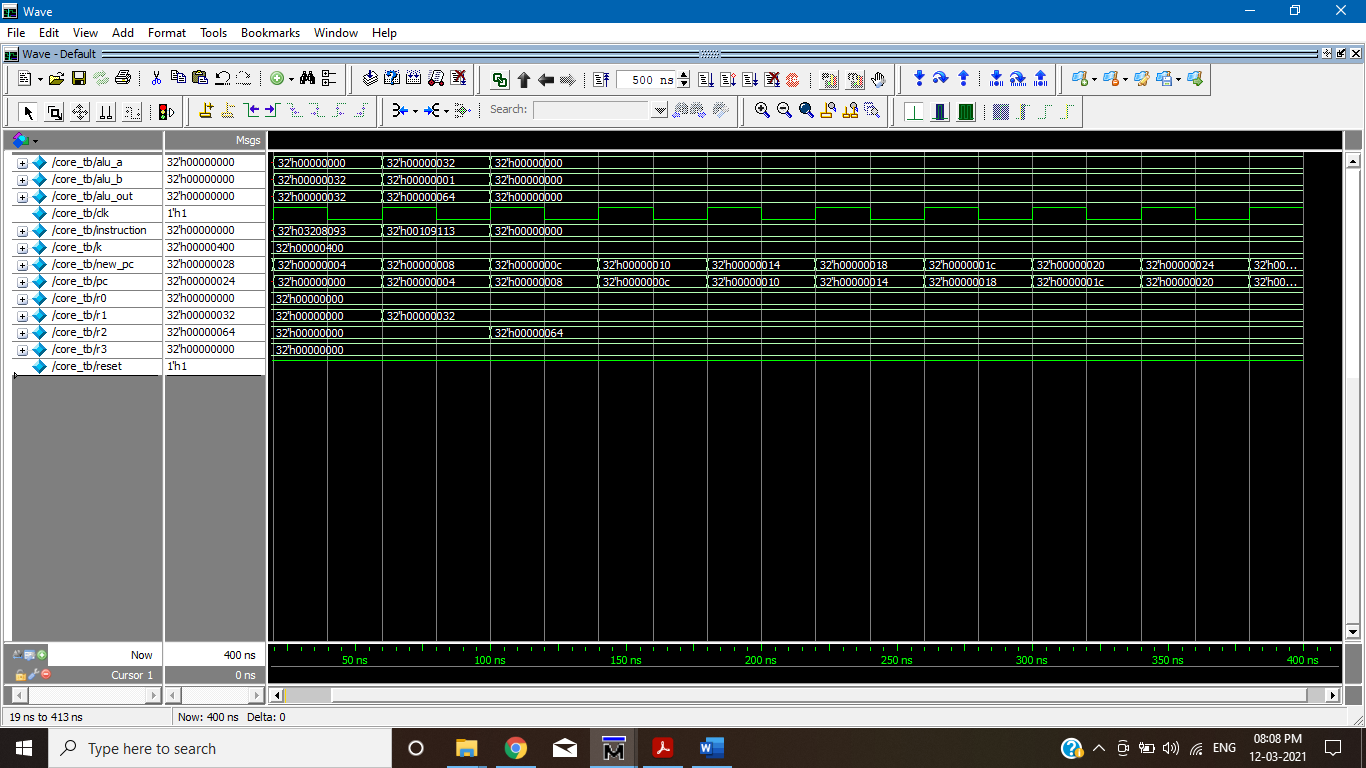
* **ORI**



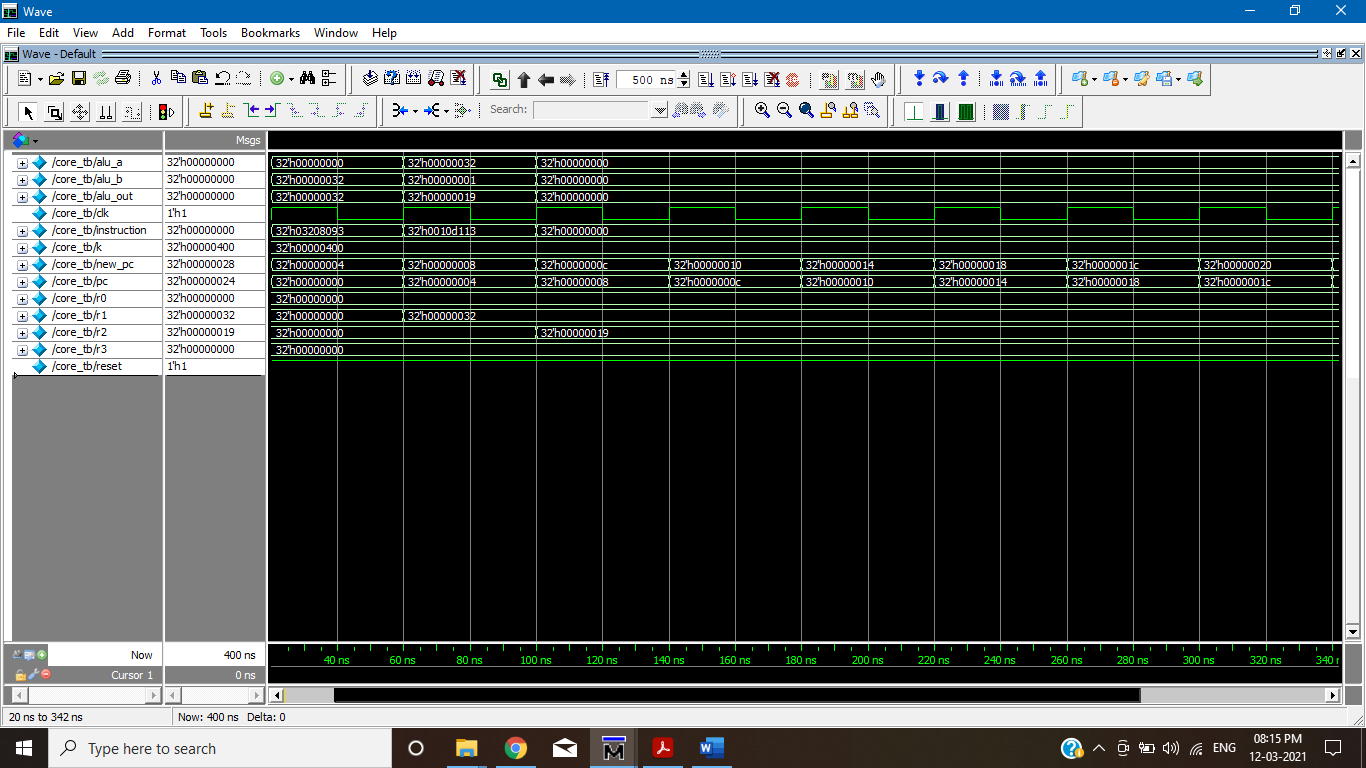
* **ANDI**



* **SLLI**



* **SRLI**



* **SRAI**

